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Title of Invention	VOLTAGE DIVIDER FOR INTEGRATED CIRCUITS							
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant: Wagdi W. Abadeer</p> <p>Confirmation Number:</p> <p>Attorney Docket Number: BUR920030053US1</p>								
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<table border="1"><thead><tr><th data-bbox="131 993 773 1031">Submitted By:</th><th data-bbox="773 993 1110 1031">Elec. Sign.</th><th data-bbox="1110 993 1412 1031">Sign. Capacity</th></tr></thead><tbody><tr><td data-bbox="131 1031 773 1108">Richard A. Henkler Registered Number: 39,220</td><td data-bbox="773 1031 1110 1108">Richard A. Henkler /s/</td><td data-bbox="1110 1031 1412 1108">Attorney</td></tr></tbody></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Richard A. Henkler Registered Number: 39,220	Richard A. Henkler /s/	Attorney
Submitted By:	Elec. Sign.	Sign. Capacity						
Richard A. Henkler Registered Number: 39,220	Richard A. Henkler /s/	Attorney						

Documents being submitted:	Files
us-assignment	BUR920030053US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920030053US1assign1.tif BUR920030053US1assign2.tif
us-request	BUR920030053US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920030053US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	BUR920030053US1-usidst.xml us-ids.dtd us-ids.xsl
application-body	BUR920030053-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920030053table1.tif BUR920030053table2.tif BUR920030053table3.tif BUR920030053table4.tif BUR920030053table5.tif BUR920030053table6.tif BUR920030053table7.tif

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us-declaration	BUR920030053US1dec1.tif
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us-declaration	BUR920030053US1dec3.tif
us-declaration	BUR920030053US1dec4.tif
Comments	